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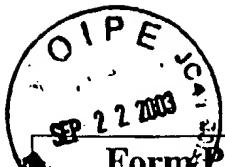
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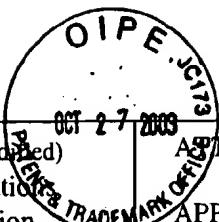


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<p>Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)</p>					OCT 27 2003 TRADEMARK OFFICE APPTY. DKT. NO. 5181-68300 APPLICANT: Lee FILING DATE: January 4, 2001		SERIAL NO. 09/755,479 GROUP: 2155	
U.S. PATENT DOCUMENTS								
EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE	
mm	E1	5,646,936	7/8/97	Shah, et al.	—	—		
mm	E2	5,682,479	10/28/97	Newhall, et al.	—	—		
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Technology Center 2100								
FOREIGN PATENT DOCUMENTS								
EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO	
mm	E3	99/26429	5/27/99	WO	—	—		
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								

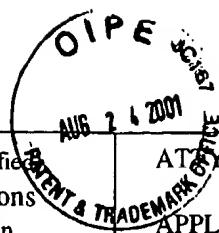
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U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
mjw	A1	6,016,510	1/18/00	Quattromani, et al.	—	—	
mjw	A2	6,023,753	2/8/00	Pechanek, et al.	—	—	
mjw	A3	5,689,661	11/18/97	Hayashi, et al.	—	RECEIVED	
mjw	A4	6,167,502	12/26/00	Pechanek, et al.	—	—	AUG 27 2001
mjw	A5	6,101,181	8/8/00	Passint, et al.	—	—	
mjw	A6	5,720,025	2/17/98	Wilkes, et al.	—	Technology Center 2100	
mjw	A7	5,970,232	10/19/99	Passint, et al.	—	—	
mjw	A8	6,055,618	4/25/00	Thorson	—	—	
mjw	A9	5,701,416	12/23/97	Thorson, et al.	—	—	
mjw	A10	5,737,628	4/7/98	Birrittella, et al.	—	—	
mjw	A11	5,689,646	11/18/97	Thorson	—	—	
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EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
mjw	A12	99/26429	5/27/99	WO	—	—	
mjw	A13	0 785 512	7/23/97	EP	—	—	
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mjw	A14	Bradley Kuszmaul, Mercury Computer Systems, Inc., "The RACE Network Architecture," (posted at www.mc.com/techlit/#tech_brief prior to this), 6 pages.
mjw	A15	R.Y. Wang, T.E. Anderson and D.A. Patterson, "Virtual Log Based File Systems For a Programmable Disk," Proc. Third Symposium on Operating Systems Design and Implementation, February 1999 (Also appeared as University of California Technical Report CSD-98-1031, 16 pages).
mjw	A16	Prasant Mohapatra, "Wormhole Routing Techniques for Directly Connected Multicomputer Systems, ACM Computing Surveys, Vol. 30, No. 3, September 1998, 37 pages.
mjw	A17	Christopher Glass and Lionel Ni, "The Turn Model for Adaptive Routing," Journal of the Association for Computing Machinery, Vol. 41, No. 5, September 1994, pp. 874-902.

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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) <i>AIC 27 2001</i>			
<i>mlb</i>	A18	Reddy, Dept. of Computer & Information Sciences, "A Dynamically Reconfigurable WDM LAN Based on Reconfigurable Circulant Graph," IEEE, 1996, 4 pages.	
<i>mlb</i>	A19	Various Abstracts beginning with Funahashi, Jouraku and Amano, "Adaptive Routing for Recursive Diagonal Torus," Transactions of the Institute of Electronics, Information and Communication Engineers D-I, vol. J83D-I, no. 11, November 2000, pp. 1143-53.	
<i>mlb</i>	A20	Milan Kovacevic, Center for Telecommunications Research, "On Torus Topologies with Random Extra Links," IEEE 1996, pp. 410-418.	
<i>mlb</i>	A21	Dally, et al., The Torus Routing Chip, Distributed Computing, Springer-Verlag 1986, pp. 187-196.	
<i>mlb</i>	A22	Susan Hinrichs, "A Compile Time Model for Composing Parallel Programs," IEEE Parallel and Distributed Technology, 1995, 19 pages.	
<i>mlb</i>	A23	"CRAY T3D System Architecture Overview Manual," ftp://ftp.cray.com/product-info/mpp/T3D_Architecture_Over/T3D.overview.html , Cray Research, 1993, 40 pages.	
<i>mlb</i>	A24	Marco Fillo, et al., "The M-Machine Multicomputer," Laboratory for Computer Science, Massachusetts Institute of Technology, A.I. Memo No. 1532, Ann Arbor, March 1995, 14 pages.	
<i>mlb</i>	A25	Noakes, et al., "The J-Machine Multicomputer: An Architectural Evaluation," Proceedings of the 20 th International Symposium on Computer Architecture, May 1993, 12 pages.	
<i>mlb</i>	A26	Dally, et al., "Architecture of a Message-Driven Processor," International Conference on Computer Architecture, June 1987, pp. 189-196.	
<i>mlb</i>	A27	Dennison, Lee and Dally, "High-Performance Bidirectional Signalling in VLSI," Massachusetts Institute of Technology, October 12, 1992, 20 pages.	
<i>mlb</i>	A28	Dally, et al., "Architecture and Implementation of the Reliable Router," Mass. Institute of Technology, Proceedings of Hot Interconnects II, Stanford CA, August 1994, 12 pages.	
<i>mlb</i>	A29	Dally, et al., "The Reliable Router: A Reliable and High-Performance Communication Substrate for Parallel Computers," Proceedings of the First International Parallel Computer Routing and Communication Workshop, Seattle WA, May 1994, 15 pages.	
<i>mlb</i>	A30	Dennison, et al., "Low-Latency Plesiochronous Data Retiming," Mass. Institute of Technology, Proceedings of the 1995 Advanced Research in VLSI Conference, Chapel Hill NC, March 1995, 12 pages	
<i>mlb</i>	A31	Whay S. Lee, "Mechanism for Efficient, Protected Messaging," Massachusetts Institute of Technology, Dept. of Electrical Engineering and Computer Science, January 20, 1999, 147 pages.	
<i>mlb</i>	A32	Dennison, "Reliable Interconnect Networks for Parallel Computers," Mass. Institute of Technology, Dept. of Electrical Engineering and Computer Science, April 18, 1991, 79 pages.	
<i>mlb</i>	A33	Thucydides Xanthopoulos, "Fault Tolerant Adaptive Routing in Multicomputer Networks," Dept. of Electrical Engineering and Computer Science, Mass. Institute of Technology, January 20, 1995, 152 pages.	
<i>mlb</i>	A34	Dennison, "The Reliable Router: An Architecture for Fault Tolerant Interconnect," Dept. of Electrical Engineering and Computer Science, Mass Institute of Technology, May 24, 1996, 145 pages.	

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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
mw	A35	"Introduction To Parallel Algorithms and Architectures: Arrays, Trees, Hypercubes," F. Thomson Leighton, Morgan Kaufmann Publishers, 1992, pp. 1-831.				
mw	A36	Christopher J. Glass and Lionel Ni, "Fault-Tolerant Wormhole Routing in Meshes," Technical Report, MSU-CPS-ACS-72, October 30, 1992 (revised May 25, 1993), 28 pages.				
mw	A37	Stefan Savage and John Wilkes, "AFRAID-A Frequently Redundant Array of Independent Disks," Proceedings of the 1996 USENIX Technical Conference, pp. 27-39, San Diego, CA, January 1996, 13 pages.				
mw	A38	Steve Ward, et al., "A Modular, Scalable Communications Substrate," MIT Laboratory for Computer Science, July 1993, 10 pages.				
mw	A39	Christopher Glass and Lionel Ni, "The Turn Model for Adaptive Routing," Technical Reports, MSU-CPS-ACS-44, October 10, 1991 (revised March 2, 1992), pages 278-287 (numbered herein as 1-20).				
mw	A40	Thomas Stricker, "Message Routing on Irregular 2D-Meshes and Tori," School of Computer Science, Carnegie Mellon Univ., January 15, 1991, pages 170-177 (numbered herein as 1-19).				
mw	A41	Dally, et al., "The J-Machine: A Retrospective," in 25 Years of the International Symposia on Computer Architecture - Selected Papers. Pp. 54-58				
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FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
mm	B2	669 584	2/20/95	EP	—	—	
mm	B3	550 853	12/16/92	EP	—	—	
mm	B4	99/63442	12/9/99	WO	—	—	
mm	B5	646 858	8/26/94	EP	—	—	
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mm	B7	99/17217	4/8/99	WO	—	—	
mm	B8	94/12939	6/9/94	WO	—	—	
mm	B9	439 693	8/7/91	EP	—	—	
mm	B10	92/06436	4/16/92	WO	—	—	

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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